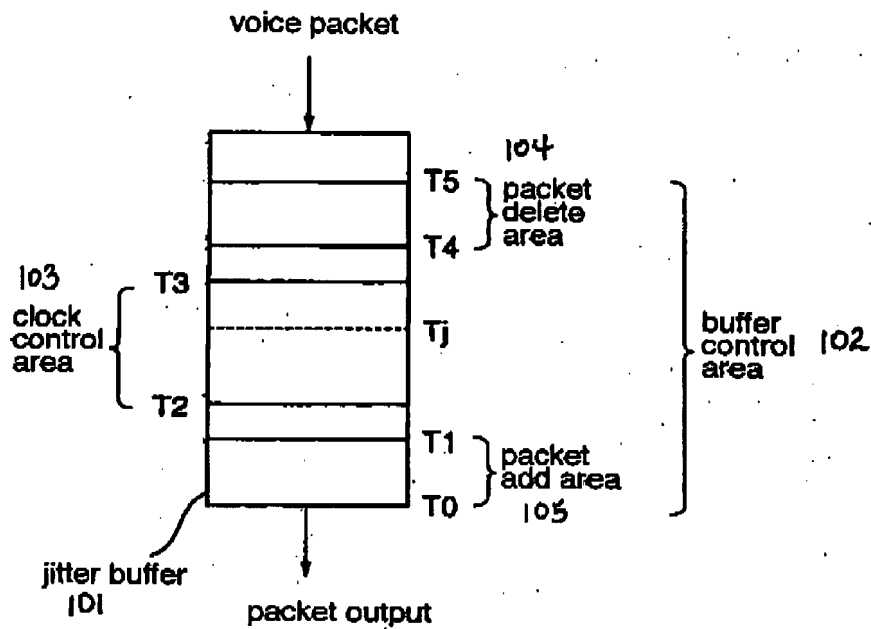


Docket No.030712-15
 Title: Control Method And Device of Jitter Buffer
 Nixon Peabody LLP, 401 9th Street, Suite 900
 Washington D.C. 20004 (202) 585-8000

1 of 1
 Replacement Drawing

FIG. 1



Tj: current stored packet quantity
 T0: lower limit of packet add area
 T1: upper limit of packet add area
 T2: lower fluctuating value of clock control area
 T3: upper fluctuating value of clock control area
 T4: lower limit of packet delete area
 T5: upper limit of packet delete area